L Number	Hits		DB	Time stamp
1	1	(controller\$1 or processor\$1 or	USPAT;	2004/02/24 18:19
		microcontroller\$1 or microprocessor\$1)	US-PGPUB;	
		near5 life\$1 near5 battery\$1 near5	EPO; JPO;	
1		voltage\$1 with tire\$1 near5 temperature\$1	DERWENT;	
			IBM_TDB	
2	2		USPAT;	2004/02/24 18:20
		microcontroller\$1 or microprocessor\$1)	US-PGPUB;	
		near5 life\$1 near5 battery\$1 near5	EPO; JPO;	
		voltage\$1 and tire\$1 near5 temperature\$1	DERWENT;	
			IBM_TDB	
3	2	(controller\$1 or processor\$1 or	USPAT;	2004/02/24 18:20
		microcontroller\$1 or microprocessor\$1)	US-PGPUB;	
1		near5 life\$1 near5 battery\$1 near5	EPO; JPO;	
		voltage\$1 and tire\$1 near65 temperature\$1	DERWENT;	
			IBM_TDB	
4	2		USPAT;	2004/02/24 18:20
		microcontroller\$1 or microprocessor\$1)	US-PGPUB;	
		near5 life\$1 near5 battery\$1 near5	EPO; JPO;	
		voltage\$1 and tire\$1 and temperature\$1	DERWENT;	
			IBM_TDB	
5	3	(controller\$1 or processor\$1 or	USPAT;	2004/02/24 18:31
		microcontroller\$1 or microprocessor\$1)	US-PGPUB;	
		near55 life\$1 near5 battery\$1 near55	EPO; JPO;	
		voltage\$1 and tire\$1 near15 temperature\$1	DERWENT;	
			IBM_TDB	